

<b>Electronic Patent Application Fee Transmittal</b>				
<b>Application Number:</b>	10749912			
<b>Filing Date:</b>	30-Dec-2003			
<b>Title of Invention:</b>	Instruction memory hierarchy for an embedded processor			
First Named Inventor/Applicant Name:	Donald E. Steiss			
<b>Filer:</b>	Amir H. Raubvogel/Larisa Burshteyn			
<b>Attorney Docket Number:</b>	22347-08261 (8117)			
Filed as Large Entity				
<b>Utility Filing Fees</b>				
Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
<b>Basic Filing:</b>				
<b>Pages:</b>				
<b>Claims:</b>				
Independent claims in excess of 3	1201	3	200	600
<b>Miscellaneous-Filing:</b>				
<b>Petition:</b>				
<b>Patent-Appeals-and-Interference:</b>				
Post-Allowance-and-Post-Issuance:				
<b>Extension-of-Time:</b>				

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension - 1 month with \$0 paid	1251	1	120	120
<b>Miscellaneous:</b>				
<b>Total in USD (\$)</b>				<b>720</b>